



MSTronic Co., Ltd.

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SPECIFICATION

MIT-51G-56

Ultra High PoE Injector

1. INPUT :

- 1.1 Input Voltage: AC 100V ~ 240V $\pm 10\%$
- 1.2 Input Frequency : 47 ~ 63Hz
- 1.3 Input Current: 1.50A at 120Vac @F.L PF > 0.98
0.6A at 230Vac @F.L PF > 0.95
- 1.4 Inrush current: 25A Max at 120Vac & 50A Max at 230Vac

2. OUTPUT :

2.1 Output Voltage & Current:

OUTPUT	+56V
Max. load	2.0A
Power	112W Max
Min. Load	0A
Load reg. %	5%
Line reg. %	1%
Ripple %	1%
Noise %	2%

TOTAL POWER :112 W

Note 1: Ripple & Noise bandwidth is from DC to 20Mhz. Terminated With a 47uF Capacitor and 0.1uf MPE Capacitor of Proper Polarity.

- 3. EFFICIENCY : 88% min. at AC 120V Input @F.L
88% min. at AC 240V Input @F.L

4. PROTECTION

4.1 Short Circuit Protection

output Short GND Terminal will not damage the Power Supply will Auto-Recover when Load status going to normally.

4.2 Over Voltage Protection : 58V ~ 60V

4.3 Over Current Limits : 120% ~ 150% @100V ~ 240Vac

4.4 LED Indicate POWER – OK



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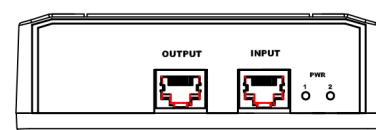
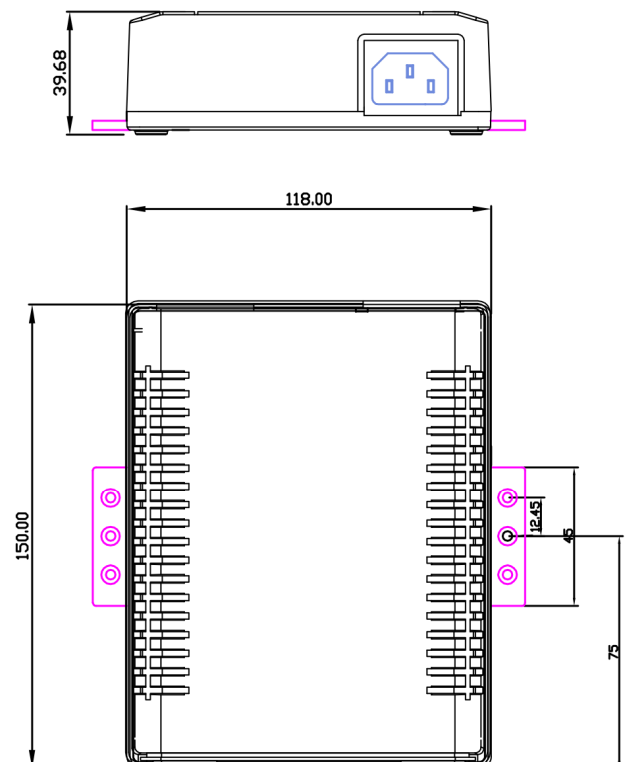
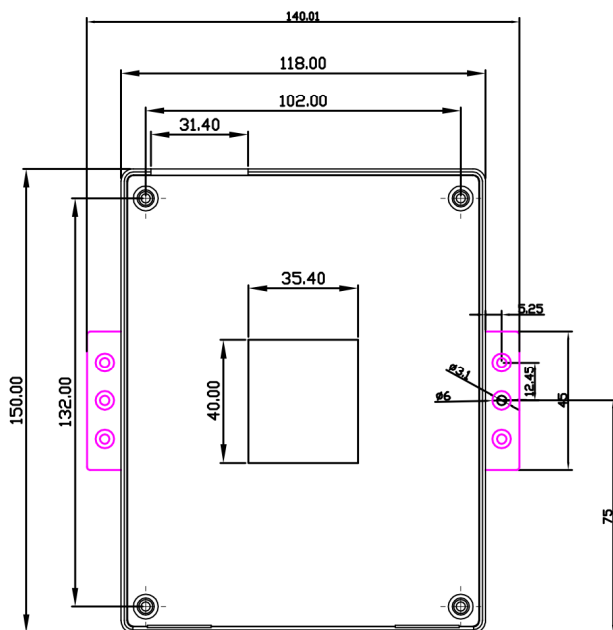
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- 5. EMC : Meet FCC Class B EN55022 Class B
- 5.1 SAFETY STANDARD : Meet UL1950, CSA 22.2 &TUV EN60950
- 5.2 Hold up time :8mS min. at maximum load &120Vac Input.

6. GENERAL DESCRIPTION

- 6.1 Operation Temperature: -40 - +50 Degree
- 6.2 Storage Temperature: -40 - +85 Degree
- 6.3 Operation Humidity: 5% - 90%
- 6.4 Cooling: Free air cooling
- 6.5 SIZE 118*150*40 (L*W*H)





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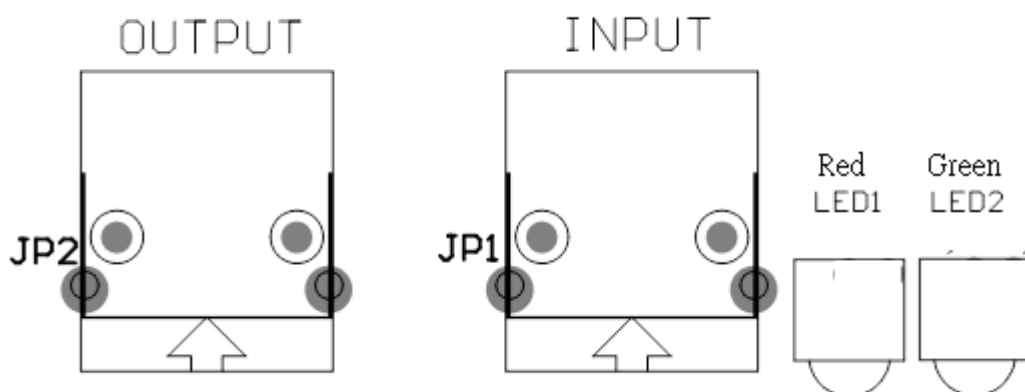
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7. RJ45 Pin Assignment : @1000M

RJ-45 Input (Data Only)			RJ-45 Output (Data & Power)	
Pin	Symbol	Description	Symbol	Description
1	BI_DA+	Data Pair A+	-Vdc + BI_DA+	power(-)+Data Pair A+
2	BI_DA-	Data Pair A-	-Vdc + BI_DA-	power(-)+Data pair A-
3	BI_DB+	Data Pair B+	+ Vdc + BI_DB+	power(+)+Data Pair B+
4	BI_DC+	Data Pair C+	+Vdc + BI_DC+	power(+)+Data Pair C+
5	BI_DC-	Data Pair C-	+Vdc + BI_DC-	power(+)+Data Pair C-
6	BI_DB-	Data Pair B-	+Vdc + BI_DB-	power(+)+Data Pair B-
7	BI_DD+	Data Pair D+	-Vdc + BI_DD+	power(-)+Data Pair D+
8	BI_DD-	Data Pair D-	-Vdc + BI_DD-	power(-)+Data Pair D-
9	SHLD	Shield enclosure	SHLD	Shield enclosure

8. INDICATOR :'

1. LED2(GREEN) Indicate POE Output is OK





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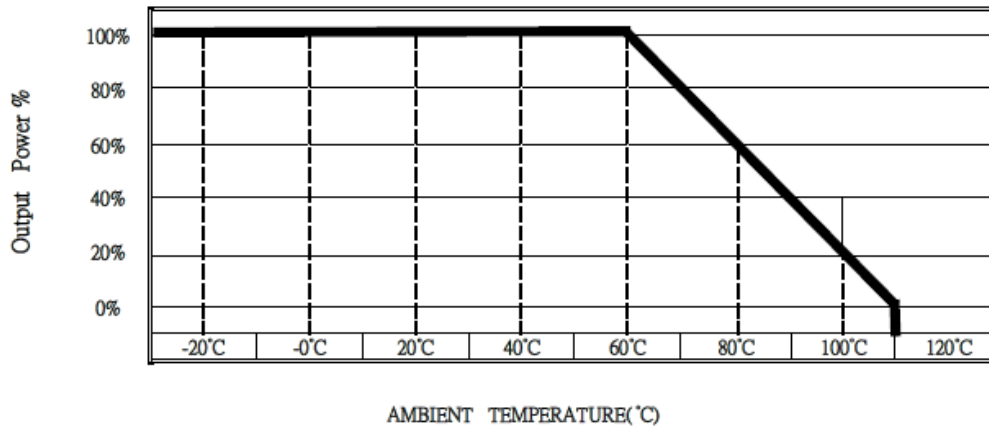
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RATING TEMPERATURE LIMITS AND OUTPUT POWER RANGE



Model Name: MIT-51G-56

Date: 03 / 10/ 2016

Input Voltage : 100 ~ 240 Vac

Output: 56V / 2 A

INPUT VOLTAGE EFFICIENCY	AC 90	AC 120	AC 230	AC 264
25%	85.95%	85.75%	83.21%	82.48%
50%	89.55%	89.73%	89.14%	88.92%
75%	89.90%	90.31%	90.89%	90.76%
100%	88.78%	90.24%	91.72%	91.69%
Average	88.55%	89.01%	88.74%	88.46%

LEVEL VI PERFORMANCE THRESHOLDS

The Level VI standard tightens performance thresholds for adapters in active mode and no-load conditions. Along with tightened regulations for adapters governed under previous regulations, the new standard expands the range of products that fall under the standard. Regulated products will now include:

- Multiple-voltage external power supplies
- Products with power levels >250 watts

The latest performance thresholds are summarized in the tables below:

SINGLE-VOLTAGE EXTERNAL AC-DC POWER SUPPLY ¹ , BASIC-VOLTAGE		
Nameplate Output Power (P _{out})	Minimum Average Efficiency in Active Mode (expressed as a decimal)	Maximum Power in No-Load Mode [W]
P _{out} ≤ 1 W	0.5 + P _{out} + 0.16	0.100
1 W < P _{out} ≤ 49 W	0.671 + ln(P _{out}) - 0.0014 × P _{out} - 0.67	0.100
49 W < P _{out} ≤ 250 W	0.880	0.210
P _{out} > 250 W	0.875	0.500